



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN THE APPLICATION OF:

**WILLIAM J. BORLAND ET AL.**

CASE NO.: **EL0496 US NA**

APPLICATION NO.: **10/663,551**

CONFIRMATION NO.: **2580**

GROUP ART UNIT: **2831**

EXAMINER:

FILED: **SEPTEMBER 16, 2003**

FOR: **PRINTED WIRING BOARDS HAVING LOW INDUCTANCE EMBEDDED  
CAPACITORS AND METHODS OF MAKING SAME**

**SUPPLEMENTAL INFORMATION  
DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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In compliance with 37 CFR 1.97 and 1.98, Applicant would like to bring to the attention of the U.S. Patent and Trademark Office information that may be helpful in the examination of the above-identified patent application. All of the information is listed on an attached Form PTO/SB/08.

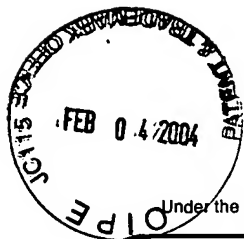
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Respectfully submitted,

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Dated: 1/30/2004

Enclosures



PTO/SB/08b (08-03)

Approved for use through 06/30/2006. OMB 0651-0031

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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

**Complete If Known**

Application Number	10/663,551
Filing Date	September 16, 2003
First Named Inventor	William J. Borland
Art Unit	2831
Examiner Name	
Attorney Docket Number	EL0496 US NA

Sheet 1 of 1

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		DR. KIM FJELDSTED AND STACEY L. CHASE, EMBEDDED PASSIVES: LASER TRIMMED RESISTORS, CIRCUITREE, Volume 70, March 2002.	<input type="checkbox"/>
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10/663,551

EL0496 US NA

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

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